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In the United States Patent and Trademark Office

Serial number 09/340,172

Application filed June 25, 1999

Applicant Derek Wong

Application Title Methods for Increasing Instruction-Level Parallelism in Microprocessors and Digital Systems

Examiner/GAU Eric Coleman / 2183

Mailed February 19, 2005

at San Jose, California

From:

Derek Wong

1341 Echo Valley Dr.

San Jose, CA 95120

To:

Assistant Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RE: Amendment E to patent application 09/340,172

Thank you for reviewing patent application 09/340,172.

In response to the office action letter regarding patent application 09/340,172 mailed on November 19, 2004, the applicant encloses an amended set of claims and this letter.

The amended set of claims has the following changes:

Claims 78, 84, and 85 have been amended.

The applicant respectfully submits the following comments in response to the office action:

1. In the interest of making the claim set ready for allowance, claims 78, 84, and 85 have been amended to be dependent claims upon claim 44, which has been previously allowed.

2. Minor wording changes to 84 and 85 to assert a limitation without use of the word "can" have also been included.

The applicant respectfully submits that the currently amended claims should be allowable over the prior art.

The applicant has reviewed the references cited in previous office actions. These references do not appear to describe the presently claimed inventions and do not appear render the claimed inventions to be obvious in any way.

The applicant is available at telephone number 408-927-7940 to discuss this application.

Thank you for your review of this application and thank you for your help.

Very respectfully,



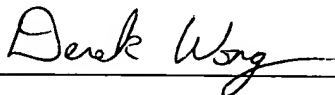
Derek Wong

Patent Applicant

Express Mail Label # EU571761851US Date of Deposit February 19, 2005

I hereby certify that this paper or fee is being deposited with the United States Postal Service using "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to "Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450."

Signed



Inventor